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IN THE UNITED STATES PATENT AND TRADEMARK OFFECHNOLOGY CENTER 2800

Applicant

: Thomas Peter Haneder et al.

Applic. No.

: 09/801,209

Filed

: March 7, 2001

Title

: Ferroelectric Transistor, Use Thereof In A Memory Cell Configuration And Method Of Producing The Ferroelectric Transistor

Examiner

: Thao X. Le

Group Art Unit: 2814

AMENDMENT

Hon. Commissioner of Patents and Trademarks, Washington, D. C. 20231

Sir:

Responsive to the Office action dated September 23, 2002 kindly amend the above-identified application as follows:

In the Claims:

Claim 1 (amended). A ferroelectric transistor, comprising:

A